ICL7135 Pin 11 +5V, Pin 1 -5V, Pin 24 digital GND

ICL7135 Intersil, 8751-8031-8951, the circuit for uC chips are not drawn as they are in other circuits. or see datasheets.

del20001.sch1

Interface 7135 to 8751 to read analog values

Use Only Low Leakage Multilayer Plastic Capacitors for A-D convertors of Intersil like 7107, 7109, 7106, 7135. use MFR 1% for all Resistors, 33E means 33 ohms, 22K means 22 kilo ohms and 4K7 is 4.7 K. ‘474 CD’ means 47 with 4 zeros pF, 470000 pF, remove 3 zeros 470 nF, shift decimal 3 places again 0.47uF.

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http://delabs.topcities.com/
http://delabs.netfirms.com/
http://delabs.tripod.com/
http://geocities.com/delabs/
http://members.fortunecity.com/delabs/

In - negative
In + positive

+1.9999 V DC max

U2B sends a short interrupt pulse to uC which means 'busy' of 7135 has gone from high to low and reading in counter of uC is valid, also reset counter to 0 for new reading next time 'busy' goes high. count the clock and subtract 10,001 from that, you got a reading.

ICL7135 Pin 8 GND

BUSY 'AND' CLK '-' 10,001 '=' READING

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1.0000 V +/- 1.9999 V DC max

zeners pA leakage but protects.

+5V