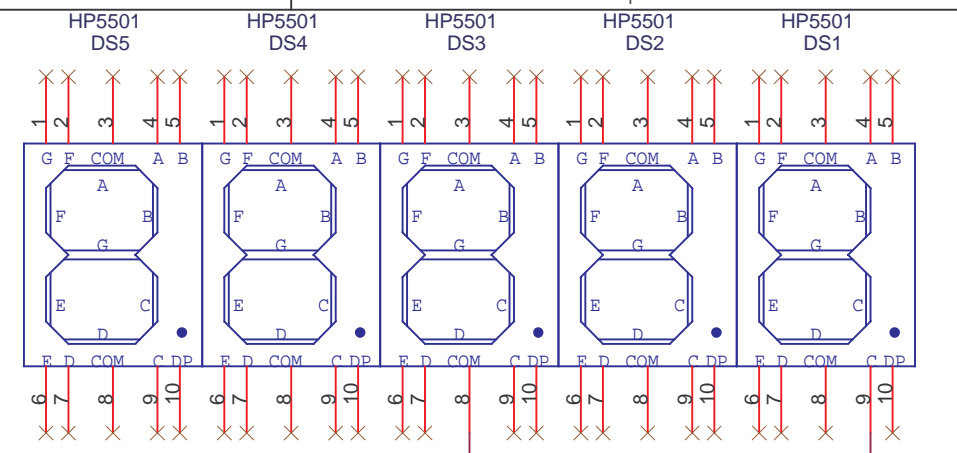


The Circuit Is Shown for One Anode Drive and one Segment Drive for Display. In similar Fashion connect rest, all 5 anodes and 7 segments and one dp decimal point.

The +5 V of 2N2907 and gnd of BC547 must be directly from regulator with a big cap or even a separate supply.



ICL7135 Pin 11 +5V, Pin 1 -5V, Pin 24 digital GND, add a decoupling 104 CD cap from +5 to GND and -5 to GND

In + positive
In - negative

add zeners (pA leakage but protects.)

+/- 1.9999 V DC max

AT89C52 pin 20 gnd pin 40 +5V

Pin 16 +5V and Pin 15 Gnd.

This circuit is only a design concept, It is not tested, It could have errors.

use 74HCT40XX chips in place of CD40XX for only 5V and high speed designs. Use Shrink sleeves and proper gauge of wire. put 104 CD cap for all ICs from positive to negative close to IC, even if omitted in circuit, for opamps on dual supply two caps. unused inputs of logic and opamps pull up or down to avoid oscillations and noise. connect supply of all chips if not mentioned. "analog ground" and "digital ground" must be linked at power supply only, avoid loops, let grounds radiate from a ground plane. use MFR 1% for all Resistors, 33E means 33 ohms, 22K means 22 kilo ohms, 1M is 1 megohm. 10T tp means ten turn trimpot. '474 CD' is 47 with 4 zeros pF, 470000 pF, 470 nF, 0.47uF, ceramic disc. "pl" is plastic, low leakage multilayer.



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