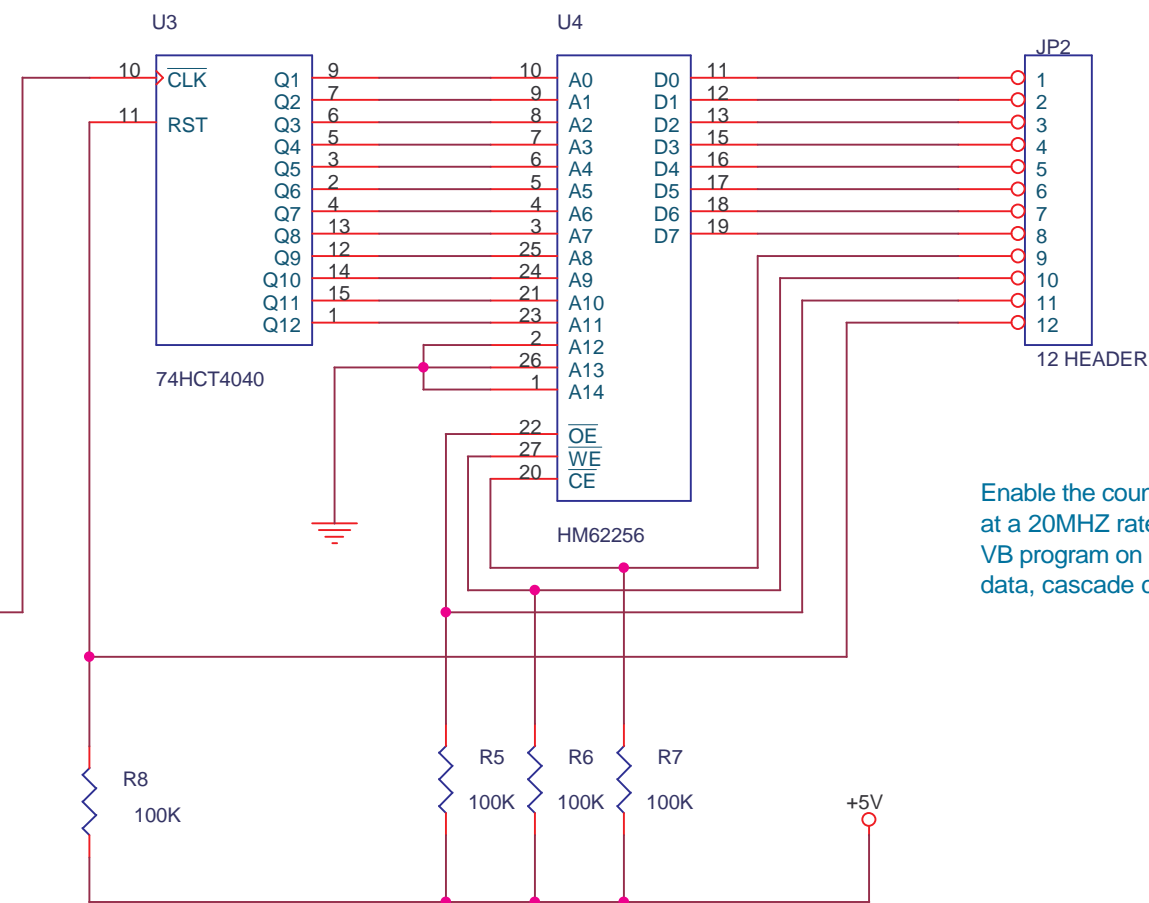
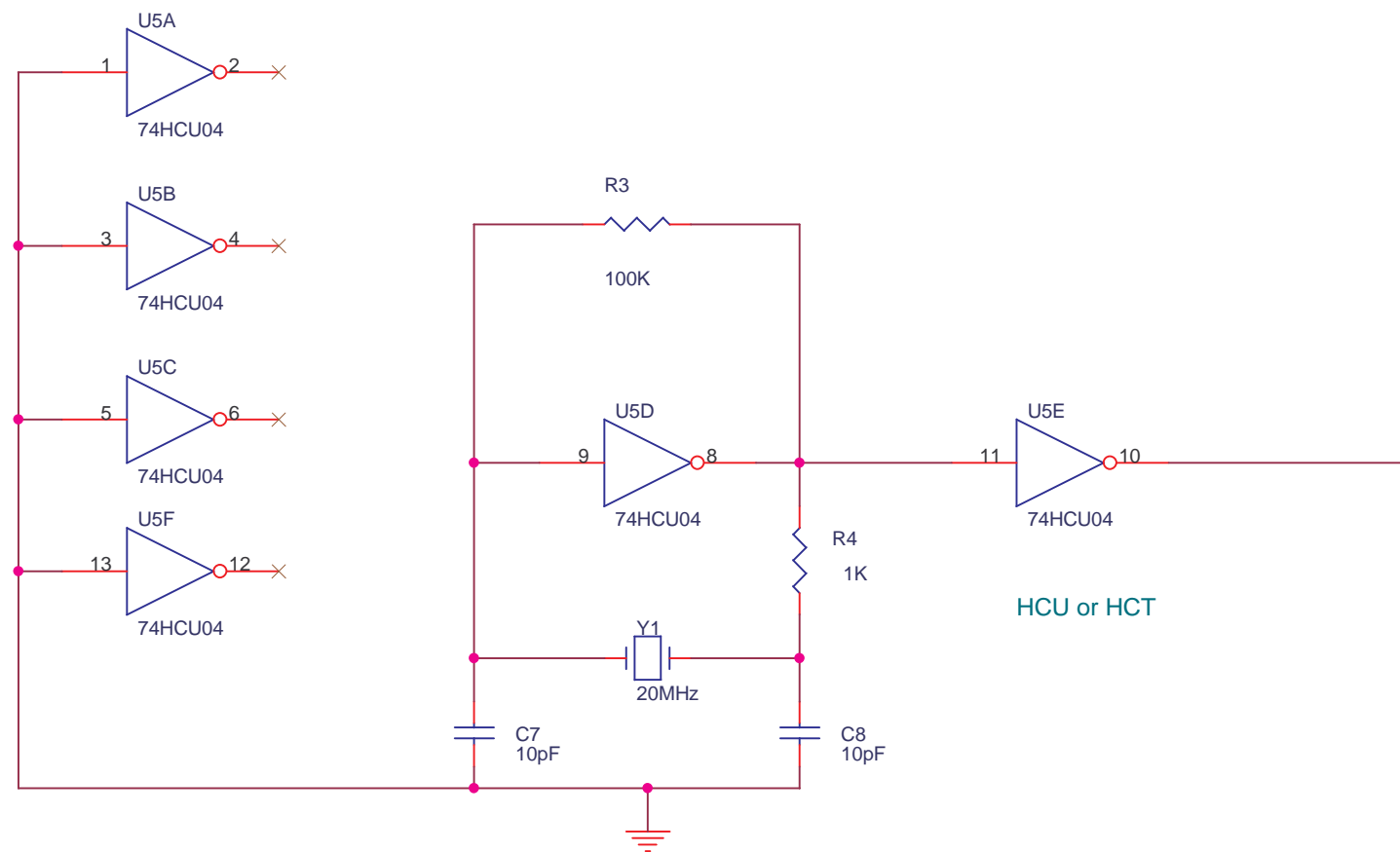


Logic Spooler for 10MHz Logic Analyzer
(this is just a design idea for experts)



Interface to Printer Port, This you have to Design



Enable the counter from PC and read Logic inputs to RAM
at a 20MHz rate. Now read RAM at a slow rate into your
VB program on PC. use OLEDB or MSFLEX grid to study
data, cascade counters RAM as you please.

unused inputs of chips and opamps etc. pull up or down to avoid oscillations and noise pickup.

"analog ground" and "digital ground" must be linked at power supply only, avoid loops, let grounds radiate from a ground plane.

use MFR 1% for all Resistors, 33E means 33 ohms, 22K means 22 kilo ohms, 1M is 1 megohm. 10T Trimpot means like 10 turn bourns.

'474 CD' is 47 with 4 zeros pF, 470000 pF, remove 3 zeros 470 nF, shift decimal 3 places 0.47uF. "pl" is plastic, low leakage multilayer.

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Title		
Logic Analyzer		
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